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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10064454	FILING DATE 07/17/2002	CLASS 711	SUBCLASS 220	GAU 2186	EXAMINER J. Ross
**APPLICANTS: Kuo Kuang-Kai; Liu Kuo-Ping;					
**CONTINUING DATA VERIFIED: <i>✓</i>					
**FOREIGN APPLICATIONS VERIFIED: <i>✓</i> TAIWAN 91103729 03/01/2002					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 8327-US-PA	
Verified and Acknowledged Examiners's initials <i>✓</i>					
TITLE : Control chip and method for accelerating memory access					

U.S. DEPT. OF COMM / PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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